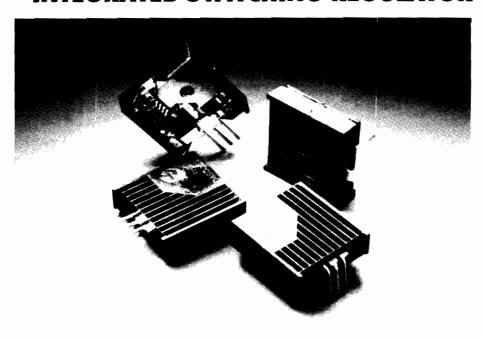
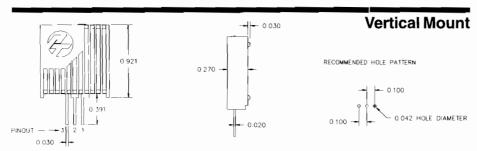
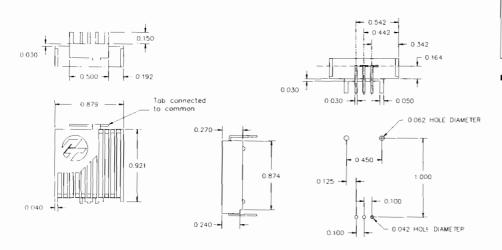
78SR Series

1.5 AMP POSITIVE STEP-DOWN INTEGRATED SWITCHING REGULATOR





Horizontal Mount



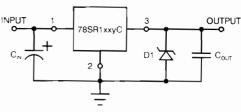
78SR "C" Series is a new line of 3-Terrninal Integrated Switching Regulators (ISR's) that are as easy to use as linear 3-terminal regulators. These ISR's have a maximum output current of 1.5 Amps and an output voltage that is laser trimmed to industry standard voltages. They have excellent line and load regulation with internal short circuit and over-temperature protection.

- State-of-the-Art Power Density90 Watts per Cubic Inch
- High Efficiency >85%
- Self-Contained Inductor
- Internal Short Circuit and Over-Temperature Protection
- Pin Compatible with Existing Linear 3-Terminal, "78" Series Regulators

Pin-Out Information

| 78SR "C" Series Positive Regulators | | | | | | |
|-------------------------------------|---|--------|----------|--|--|--|
| Pin | 1 | INPUT | (right) | | | |
| | 2 | COMMON | (center) | | | |
| | 3 | OUTPUT | (left) | | | |
| (As viewed from the finned side) | | | | | | |

Standard Application

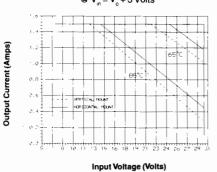


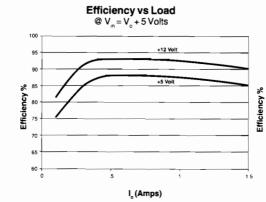
- C_{IN} = Optional low ESR electrolytic (10µF)
 - = IN53xxB zener required to clamp turn-on overshoot
- ουτ = Optional ceramic (1μF)

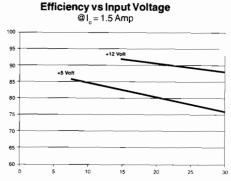
Performance Data

Temperature Derating
For small airflow and recommended PCB layout.

@ V_{.n} = V_o + 3 Volts







Input Voltage (VDC)

Specifications

| Characteristics | | | <u> </u> | 70004 | | | |
|--------------------------------------------------------|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------|------------------|-----------------|------|------------------|--|
| Characteristics (T _a =25°C unless noted) | Symbols | Conditions | | 78SR1xxyC | | Units | |
| (1 _a -23 o dilless floted) | | | Min | Тур | Max | | |
| | | Over V _{in} range | 0.1 | <u> </u> | 1.5 | Amps | |
| Current Limit | l _c ı | V _{in} =V _o +3V | - | 1.8 | - | Amps | |
| Short Circuit Current | I _{sc} | V _{in} =V _o +3V | • | 3.5 | - | Amps | |
| Input Voltage Range | Vin | I₀=1.5 Amp V₀=5V I₀=1.5 Amp V₀=12V | V₀+2V V₀+2.5V | - | 30 | Volts | |
| Reflected Ripple | l _{tt} | $V_{in}=V_{o}+5V$, $I_{o}=1$ Amp $Z_{source}=1.0\Omega$ | - | 15 | - | mA _{pp} | |
| Static Voltage Tolerance | ΔV_{\circ} | Over V _m range, I _o =1 Amp T _a =-40 to +65°C | - | ±1.0 | ±2.0 | %V _° | |
| Ripple Rejection | RR | Over V _{in} range | - | 45 | - | db | |
| Line Regulation | Regline | Over V _{in} range | - | ±0.2 | ±0.4 | %Vo | |
| Load Regulation | Regload | 0.15 ≤ I₀ ≤ 1.5 Amp | - | ±0.1 | ±0.2 | %Vo | |
| Ripple/Noise | Vn | V _{in} =8V, I ₀ =1.5 A, V ₀ =5V V _{in} =15V, I ₀ =1.5 A, V ₀ =12V | - | 50 80 | - | mV_{pp} | |
| Transient Response | t _{tr} | 50% load change V₀ ≤ 1% recovery | - | - | 100 | μsec | |
| Operating Temperature | Ta | V _{in} =V _o +3V, I _o =1.5 Amp | -40 | - | +65 | °C | |
| Storage Temperature | Ts | - | -65 | - | +150 | °C | |
| Efficiency | η | V _{in} =10V, V _o =5 Volts, I _o =1 Amp V _{in} =17V, V _o =12 Volts, I _o =1 Amp | - , | 85 90 | - | % | |
| Switching Frequency | fo | | - | 650 | - | KHz | |
| EMI/RFI | - | Over V _{in} range, I₀=1.5 Amp | N | leets FCC Class | В | - | |
| Mechanical Shock | - | - | - | - | 50 | G's | |
| Weight | - | - | - | 0.25 7.0 | - | Ounces Grams | |
| Relative Humidity | | Non-condensing | 0 | | 95 | % | |

(Specifications subject to change without prior notice)

Ordering Information

78SR1 XX Y C 1.5 Amp Positive Integrated Switching Regulator

| | XX = Output Voltage | | y = Package Style |
|------------------------|-----------------------|------------------------|-----------------------------|
| 33 = 3.3 Volts | 06 = 6.0 Volts | 12 = 12.0 Volts | V = Vertical Mount |
| 05 = 5.0 Volts | 08 = 8.0 Volts | 14 = 13.9 Volts | H = Horizontal Mount |
| 53 = 5.25 Volts | 09 = 9.0 Volts | 15 = 15.0 Volts | |





Application Note



"78SR" C SERIES 1.5 AMP ISR THERMAL CONSIDERATIONS

The "78SR" C Series Integrated Switching Regulator (ISR) by *Power Trends* is protected from thermal overload by an internal over-temperature shutdown circuit. The ISR will operate at ambient temperatures as high as 85 °C, but requires derating of either the input voltage and/or output current to do so, as shown in Figure 1. Additional cooling air or heatsinking, as described below, will significantly decrease the amount of this derating. Power dissipation in the ISR is directly related to the magnitude of the input voltage and/or output current as shown by the efficiency curves on the data sheet.

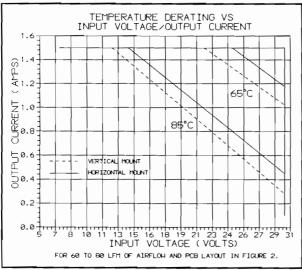


Figure 1

THERMAL SHUTDOWN SEQUENCE

When the junction temperature of the custom IC used in the ISR reaches a temperature of 140 °C, the ISR turns itself off. The ISR will automatically restart when the junction temperature cools to 130 °C. In an extreme environment, where the ambient temperature is too high for the input voltage/output current operating point, the .SR will cycle on and off continuously.

PC BOARD CONSIDERATIONS

The data used to develop Figure 1, was obtained using a 2 oz. single-sided printed circuit board with a foil layout as shown in Figure 2. The ISR is encapsulated in a thermally conductive silicon rubber which provides heat transfer to the thermally conductive plastic case. An internal copper leadframe provides excellent heat transfer to the leads. By simply increasing the copper area of the PC board attached to the leads, such as is shown in Figure 2, the ISR will conduct a significant amount of heat out through the leads using the PC board copper as a heatsink.

RECOMMENDED PC BOARD LAYOUT FOR ISR

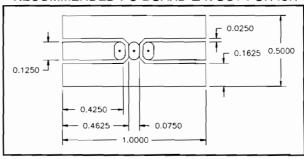


Figure 2

AIRFLOW CONSIDERATIONS

The 60 to 80 LFM noted in Figure 1 is just enough airflow to keep the air surrounding the ISR at a constant temperature, but not enough to cool it. Increasing the airflow will increase the operating range of the ISR. Airflow above 100 LFM across the ISR will dramatically improve the operating input voltage/output current/ambient temperature characteristics shown in Figure 1.

HEATSINK CONSIDERATIONS

The operating range of the ISR can be increased by using a heatsink. This can be accomplished by clamping the face (side without fins) of the ISR to a heatsink or other heat conducting surface.



Application Note



RELIABILITY 78SR SERIES 1.5 AMP ISR

Power Trends' Integrated Switching Regulators (ISR's) are designed for long reliable operation by using conservative derating factors and integral over-current and over-temperature protection. The ISR circuit utilizes a "buck" regulator topology, as shown in Figure 1. The calculations used to determine the Mean Time Between Failure (MTBF) are based on MIL-STD-217F and are conservative. Under normal operating conditions, the ISR has a calculated MTBF of over 1,000,000 hours.

78SR SERIES ISR BLOCK DIAGRAM

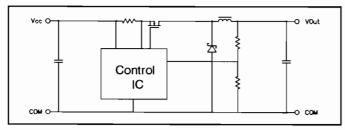


Figure 1

Construction - Power Trends' ISR is an assembly of 12 surface mount components and one integrated magnetic inductor mounted on a printed circuit board made from FR-4 material. A tin-plated, copper leadframe is soldered to the opposite side of the PC board using a high temperature solder. This leadframe is used for the power connections to the ISR. This assembly is then mounted into a plastic case, which is filled with a silicon based, thermally conductive potting compound, and sealed with a case lid. The case is molded from a high temperature plastic which is resistant to all solvents except 1,1,1 trichlorethane.

Components - All of the components used in the ISR are shown in Table 1. The components are the highest quality commercial/industrial parts available. Also shown are the components' operating characteristics and stress factor(s) when operating in a +5 VDC ISR with an input voltage of +30 VDC and an output current of 1.5 Amps. This is the worst-case operating stress that the unit will experience.

| 78SR SERIES 1.5 AMP ISR'S SAMPLE STRESS EVALUATION |
|----------------------------------------------------|
| |

| Description | Device Rating | Maximum Operating Condition | Stress Factor | | | |
|---------------------------|----------------------|-----------------------------------|------------------|--|--|--|
| Input Capacitor | 50 VDC | 30 VDC | 0.600 | | | |
| Output Capacitor | 25 VDC | 5 VDC | 0.200 | | | |
| Stability Capacitors | 50 VDC | 2.5 VDC | 0.050 | | | |
| Output Rectifier | 40 VDC | 30 VDC | 0.750 | | | |
| | 5A Avg | 1.2A Avg | 0.240 | | | |
| Power Transistor | 50 VDC | 30 VDC | 0.600 | | | |
| | 9.9 ADC 40A Pulse | 2A Peak | 0.050 | | | |
| | 42 W | 0.79 W | 0.020 | | | |
| Voltage Divider and | 100 VDC | 2.5 VDC | 0.025 | | | |
| Frequency Resistor | 62.5 mW | 0.125 mW | 0.002 | | | |
| Current Sense Resistor | 200 VDC | 0.25 VDC | 0.001 | | | |
| nesistor | 125 mW | 35 mW | 0.280 | | | |
| Inductor | 150°C | - | - | | | |
| Custom IC | 40 VDC | 30 VDC | 0.750 | | | |

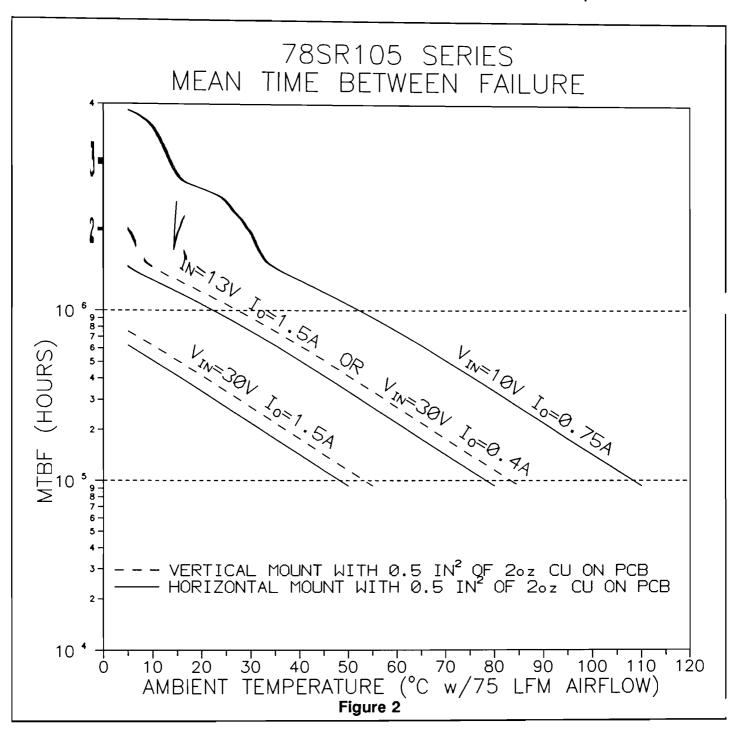
Table 1

MTBF versus ambient temperature shown in Figure 2. The environmental conditions are assumed to be ground benign. The quality factor derating multiplier for commercial, plastic case components was used.

Junction Temperature vs Operating Temperature - The internal junction temperature of the components is dependent on the operating environment and conditions. The temperature rise between the internal IC and the surrounding ambient air, without heatsinking, is

ground lead and the horizontal mounting tar are soldered to 3 to 5 square inches of copper in the ground plane. This provides an effective heatsink for the ISR and will substantially decrease its junction temperature and thereby increase its reliability.

Demonstrated MTBF - Empirical verification of the computed MTBF has continued since product introduction with no failures in over 750,000 device hours of operation with a 24 volt input and a load of 1.5 amps.



POWER TRENDS, INC. 1101 North Raddant Road Batavia, IL 60510 U.S.A.

TEL: (708) 406-0900 FAX: (708) 406-0901



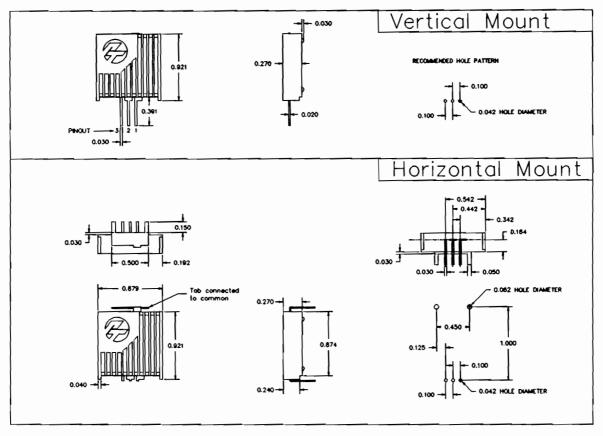
+5 to +12 VOLT/3 WATT STEP-UP INTEGRATED SWITCHING REGULATOR

The *Power Trends'* **88SR112yD** Series is a new 3-Terminal Integrated Switching Regulator (ISR) designed for use on PC boards wherever regulated +12 volts is required with up to 3 watts of output power. It is ideal for applications using a 7.2 volt battery as a main power source. This ISR has a maximum output current of 100 mAmps at an input voltage of 4.5 volts and 250 mAmps at an input voltage of 5.6 volts. The output voltage is laser trimmed and it has excellent line and load regulation. Other outstanding features include:

- Wide Input Voltage Range
 +5 to +11 Volts
- High Efficiency > 80%
- Self-Contained Inductor
- Internal Over-Temperature Protection
- 1.1 MHz Switching Frequency

| Ohoronto della | | Conditions | 8 | 88SR112yD | | |
|--------------------------------------------------------|-------------------------------------|-------------------------------------------------------------------------|-----|-------------------|-------------------|-------------------|
| Characteristics (T _a =25°C unless noted) | Symbols | Conditions (V _o = +12 Volts) | Min | Тур | Max | Units |
| Output Current | I _o | Over V _n range | 50 | - | 250 | mAmps |
| Minimum Input Voltage | Min V _n | l₀=100 mAmp l₀=150 mAmp l₀=250 mAmp | | 4.5 4.7 5.6 | 4.8 5.0 6.0 | VDC VDC VDC |
| Maximum Input Voltage | Max V _{in} | Over I _o range | - | 11.0 | - | VDC |
| Static Voltage Tolerance | V _o | Over V _n range T _a =-40 to +65°C | - | ±1.0 | ±2.0 | %V。 |
| Ripple Rejection | RR | Over V _{in} range @ 120 Hz | - | 45 | - | dB |
| Line Regulation | Reg _{line} | Over V _{in} range | - | - | ±0.6 | %V。 |
| Load Regulation | Reg _{load} | 50 ≤ l _o ≤ 250 mAmp | - | - | ±1.0 | %V。 |
| Ripple/Noise | Vn | V _{in} =+5V, I _o =100 mAmp | - | 30 | - | mV _{pp} |
| Transient Response | t _{ir} V _{max} | 0.1A to 0.2A step, V _{in} =8V, V _o ≤ 1% recovery | - | 200 200 | - | μSec mV |
| Efficiency | n | V _{in} =7.0V, I _o =200 mAmp | - | 87 | - | % |
| Switching Frequency | f _o | Over V _n and I _o ranges | 1.0 | 1.1 | 1.2 | MHz |

| | | | 88SR112yD | | | |
|--------------------------------------------------------|----------------|-----------------------------------------------|-----------|-----|-----|-------|
| Characteristics (T _a =25°C unless noted) | Symbols | Conditions (V _o =+12 Volts) | Min | Тур | Max | Units |
| Operating Temperature | T _a | Over V _n and I _o ranges | -40 | - | +65 | °C |
| Temperature Coefficent | TC | Over V _n and I _o ranges | - | TBD | | mV/°C |
| Storage Temperature | T _s | | -65 | - | 150 | °C |
| Mechanical Shock | | | _ | - | 50 | G's |
| Weight | | | | 7.3 | - | grams |
| Relative Humidity | | | 0 | - | 95 | % |



PIN-OUT INFORMATION (As viewed from the finned side)

Pin 1 INPUT (right)

2 COMMON (center)

3 OUTPUT (left)

| ORDERING INFORMATION 88SR1xxyD | | | | |
|-----------------------------------|--------------------------------|--|--|--|
| xx = Output Volts | y = Package Style | | | |
| 12 = 12.0 Volts | V = Vertical H = Horizontal | | | |



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708-406-0900



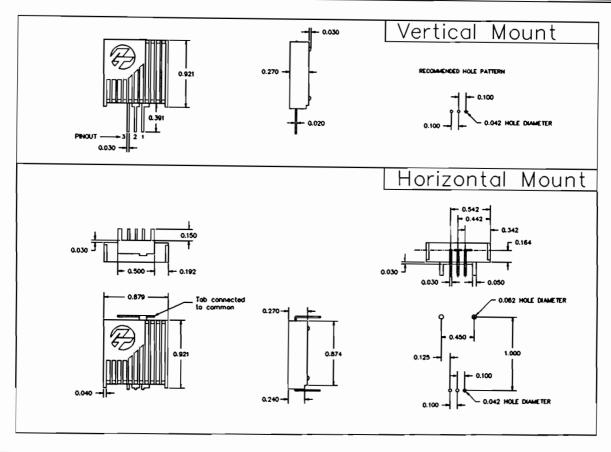
+3.3 VOLT/1.5 AMP STEP-DOWN INTEGRATED SWITCHING REGULATOR

The 78ST133yC is a new +3.3 Volt, 3-Terminal, Integrated Switching Regulator (ISR) that is as easy to use as a linear regulator. It is designed to power new low power "3 Volt" logic systems and microprocessors. These ISR's have a maximum output current of 1.5 Amps and an output voltage that is laser trimmed. They have excellent line and load regulation. Other outstanding features include:

- State-of-the-Art Power Density
 25 Watts per Cubic Inch
- High Efficiency > 80%
- Self-Contained Inductor
- Internal Short Circuit and Over-Temperature Protection
- Pin Compatible with Existing Linear 3-Terminal, "78" Series Regulators

| Observatoristics | | | 78ST133yC | | | |
|--------------------------------------------------------|---------------------|-------------------------------------------------------------------------------------------|-----------|---------|-------|----------------------------|
| Characteristics (T _a =25°C unless noted) | Symbol | Conditions | Min | Тур | Max | Units |
| Output Current | I _o | Over V _{in} range | 0 | - | 1.5 | Amps |
| Current Limit | l _{oi} | V _{in} =7V | 1.6 | 1.8 | 2.0 | Amps |
| Short Circuit Current | I _{sc} | V _{in} =7V | - | 2.4 | - | Amps |
| Input Voltage Range | V _{in} | I _o =1 Amp, V _o =3.3V | 6 | - | 20 | VDC |
| Reflected Ripple | l _{rr} | V _{in} =V _o +5VDC, I _o =1 Amp Z _{source} =1.0Ω | • | 15 | - | mA _{pp} @ 1MHz |
| Static Voltage Tolerence | V _o | Over V _{in} range, I _o =1 Amp T _a =-40 to +65° C | - | ±1.0 | ±2.0 | %V。 |
| Ripple Rejection | RR | Over V _{in} range @ 120 Hz | - | 60 | - | dB |
| Line Regulation | Regine | Over V _n range, I _o =1.5 Amp | - | ±0.2 | ±0.4 | %V。 |
| Load Regulation | Reg _{load} | 0.15 ≤ I _o ≤ 1.5 Amp | - | ±0.1 | ±0.2 | %V。 |
| Ripple/Noise | V _n | V _h =8V,I _o =1.5 Amp,V _o =3.3V | - | 50 | - | m V _{pp} |
| Transient Response (with 100μF output cap) | t _{tr} | 50% load change V _o ≤ 1% recovery | - | 50 | - | μSec |
| Efficiency | n | V _{in} =10V,I _o =1.0 Amp,V _o =3.3V | | 80 | - | % |
| Switching Frequency | f_{\circ} | Over V _{in} and I _o ranges | - | 650 | - | KHz |
| EMI/RFI | - | Over V _n and I _o ranges | Meets | FCC Cla | ass B | |

| Characteristics | | | 78ST133yC | | | |
|-------------------------------------|----------------|------------------------------------------------|-----------|-----|-----|-------|
| (T _a =25°C unless noted) | Symbols | Conditions | Min | Тур | Max | Units |
| Operating Temperature | Ta | Over V _{in} and I _o ranges | -40 | - | +65 | °C |
| Storage Temperature | T _s | | -65 | - | 150 | °C |
| Mechanical Shock | | | - | - | 50 | G's |
| Weight | | | - | 7.0 | - | grams |
| Relative Humidity | | | 0 | - | 95 | % |



PIN-OUT INFORMATION (As viewed from the finned side)

Pin 1 INPUT (right)

2 COMMON (center)

3 OUTPUT (left)

| ORDERING INFORMATION 78ST1xxyC | | | | | |
|-----------------------------------|--------------------------------|--|--|--|--|
| xx = Output Volts | y = Package Style | | | | |
| 33 = 3.3 Volts | V = Vertical H = Horizontal | | | | |

Important Note:

For proper operation, the 78ST133yC must be used with a $100\mu F$ electrolytic capacitor wired from the output to ground.



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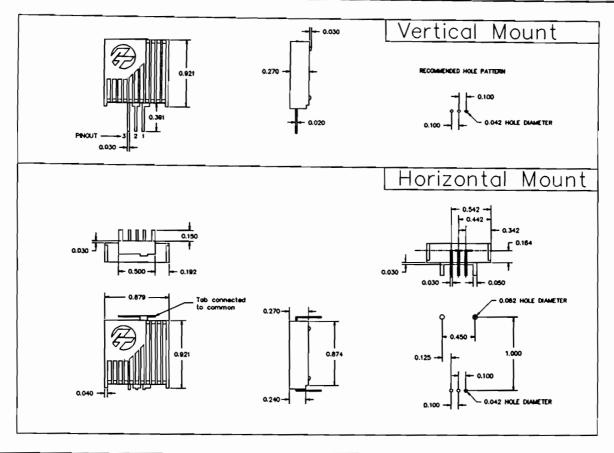
1.5 AMP *NEGATIVE* STEP-DOWN INTEGRATED SWITCHING REGULATOR

The 79SR"C" Series is a line of *Negative Input/Negative Output* 3-Terminal Integrated Switching Regulators (ISR's) that are as easy to use as linear 3-terminal regulators. These ISR's have a maximum output current of -1.5 Arnps and an output voltage that is laser trimmed to most industry standard voltages. They have excellent line and load regulation. Other outstanding features include:

- State-of-the-Art Power Density
 90 Watts per Cubic Inch
- High Efficiency > 85%
- Self-Contained Inductor
- Internal Short Circuit and Over-Temperature Protection
- Pin Compatible with Existing Linear 3-Terminal, "79" Series Regulators

| | | | 79SR1xxyC | | | |
|--------------------------------------------------------|---------------------|-------------------------------------------------------------------------------------|--------------------------------------------|---------|-------|----------------------------|
| Characteristics (T _a =25°C unless noted) | Symbol | Conditions | Min | Тур | Max | Units |
| Output Current | I _o | Over V _{in} range | -0.1 | - | -1.5 | Amps |
| Current Limit | Ici | $V_{in} = V_o - 3V$ | -1.6 | -1.8 | -2.0 | Amps |
| Short Circuit Current | I _{sc} | $V_{ln} = V_o - 3V$ | - | -3.5 | - | Amps |
| Input Voltage Range | V _{in} | I _o =-0.1 to -1.5 Amp, V _o =-5V V _o =-15V | V _o -2.5 V _o -3.5 | - | -30 | VDC |
| Reflected Ripple | l,, | $V_{in}=V_{o}$ -5VDC, $I_{o}=-1$ Amp $Z_{source}=1.0\Omega$ | - | 15 | - | mA _{pp} @ 1MHz |
| Static Voltage Tolerence | V _o | Over V _{in} range, I _o =-1 Amp T _a =-40 to +65° C | - | ±1.0 | ±2.0 | %V。 |
| Ripple Rejection | RR | Over V _{in} range @ 120 Hz | - | 45 | - | dB |
| Line Regulation | Reg _{line} | Over V _{in} range | - | ±1.0 | ±2.0 | %V。 |
| Load Regulation | Reg _{load} | -0.15 ≤ I _o ≤ -1.5 Amp | - | ±0.2 | ±0.4 | %V。 |
| Ripple/Noise | V _n | $V_{ln} = -15V, I_{o} = -1.0 \text{ Amp}, V_{o} = -5V$ | - | 35 | - | mV _{pp} |
| Transient Response | t _{tr} | 50% load change V _o ≤ 1% recovery | - | 100 | - | μSec |
| Efficiency | n | V _{In} =-10V,I _o =-1.0 Amp,V _o =-5V | - | 87 | - | % |
| Switching Frequency | f _o | Over V _{in} and I _o ranges | 0.95 | 1.0 | 1.05 | MHz |
| EMI/RFI | - | Over V _{in} and I _o ranges | Meets | FCC Cla | ass B | |

| Characteristics | | | 79SR1xxyC | | | |
|-------------------------------------|----------------|------------------------------------------------|-----------|-----|-----|-------|
| (T _a =25°C unless noted) | Symbols | Conditions | Min | Тур | Max | Units |
| Operating Temperature | T _a | Over V _{in} and I _o ranges | -40 | - | +65 | °C |
| Temperature Coefficent | TC | Over V _n and I _o ranges | - | 0.5 | 1.5 | mV/°C |
| Storage Temperature | Ts | | -65 | - | 150 | °C |
| Mechanical Shock | | | - | - | 50 | G's |
| Weight | | | - | 7.0 | - | grams |
| Relative Humidity | | | 0 | - | 95 | % |



PIN-OUT INFORMATION (As viewed from the finned side)

Pin 1 COMMON (right)

2 INPUT (center)

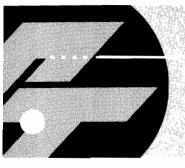
3 OUTPUT (left)

| ORDERING INFORMATION 79SR1xxyC | | | | |
|-------------------------------------------------------------------------------------------|--------------------------------|--|--|--|
| xx = Output Volts | y = Package Style | | | |
| 05 = -5.0 Volts 52 = -5.2 Volts 09 = -9.0 Volts 12 = -12 Volts 15 = -15 Volts | V = Vertical H = Horizontal | | | |



1101 North Raddant Road, Batavia, IL 60510

708-406-0900



Application Note



USING THE "78SR" C SERIES 1.5 AMP INTEGRATED SWITCHING REGULATOR (ISR)

The 78SR1xxyC Integrated Switching Regulator (ISR) by *Power Trends* is a complete switch-mode power regulator capable of delivering regulated +3.3, 5, 6, 8, 9, 10, 12, 13.9, or 15 VDC at currents up to 1.5 Amps with input voltages ranging from V_o+2 to +30 VDC for the +5 VDC unit to $V_o+2.5$ to +30 VDC for +12 VDC units and above. The ISR utilizes current mode control in a Buck regulator topology operating at a nominal frequency of 650 kHz.

Input Source Requirements

Power Trends' ISR does not incorporate a "soft start" circuit. This may result in large transient voltages briefly appearing at the output of the ISR (usually less than 100 μ sec) when it is first turned on. To prevent this situation from harming voltage sensitive loads, a 5 watt zener diode, 1N53xxB or equivalent, should be placed across the output terminals of the ISR. This zener will also provide effective over-voltage protection for the load. Also, and electrolytic capacitor can be placed across the input terminals to reduce the amount of "upstream" ripple. In applications with large load transients, a "78ST" series ISR will provide much better trasient response.

Battery and Low Input Voltage Operation

Unlike a linear regulator where the output voltage decreases as V_{in} decreases, ISR's have a minimum input voltage threshold. The control IC inside the package will not run below an input voltage of +6 VDC. Between +6 VDC and $V_{o}+2$ in ($V_{o}+2.5$ for +12 VDC and above) the output voltage will be below the specified output voltage and will not be regulated. The actual output voltage will be a function of load and the input voltage.

Ripple and Noise

Typically, the +5 VDC ISR has an output ripple/noise of 50 mV_{pp} (@ I_o = 1.5 Amp, V_{in} = +8 VDC). This output ripple/noise increases linearly with increasing input voltage. To reduce the amount of output ripple/noise, additional output capacitance may be added directly at the terminals of the ISR only if the capacitors are ceramic and the total capacitance is below 5 μ F. Adding a 1 μ F ceramic capacitor will decrease the output ripple/noise by 33%. An aluminum electrolytic placed at this point may cause the ISR to oscillate. Bypass capacitors (electrolytic or other) placed at least 2" away from the ISR will not affect its operation.

Over-Temperature Protection

When the internal temperature of the ISR reaches 140°C the ISR will automatically shut down. The ISR will automatically restart when the internal temperature cools below 130°C.

Over-Current Protection

Two independent output current detection circuits protect the ISR from damage if the output is over-loaded or shorted. The first circuit limits the output current to a maximum of 2 Amps. The second circuit shuts down the ISR if the output current reaches 3.5 Amps. The unit will automatically restart 10 µsec after the over-load or short circuit condition is removed.



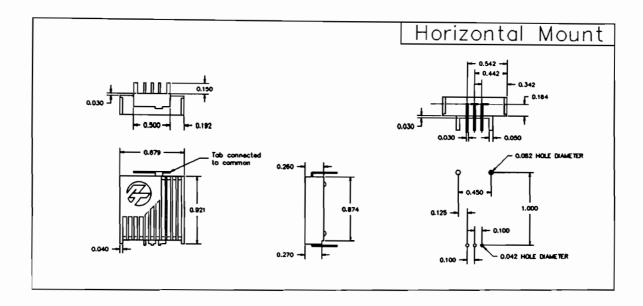
THE NOTEBOOK COMPUTER POWER CHIP

The Notebook Computer Power Chip (p/n 78ST305PC) is a new 3-Terminal Integrated Switching Regulator (ISR) designed for use as the primary +5V power supply in laptop and notebook computers or other battery operated equipment. This ISR has a typical continuous output current of 2 Amps with surge capability of 4 Amps when the recommended PC board layout is used as a heatsink. The output voltage, operating frequency, and current limit are laser trimmed and 100% computer tested. Other outstanding features include:

- State-of-the-Art Power Density
 50 Watts per Cubic Inch
- High Efficiency > 85%
- Self-Contained Inductor
- Internal Short Circuit and Over-Temperature Protection
- 1 MHz Switching Frequency

| Observatoristics | | | 78ST305PC | | | |
|--------------------------------------------------------|---------------------|-------------------------------------------------------------------------------------------|-----------|------|------|----------------------------|
| Characteristics (T _a =25°C unless noted) | Symbols | Conditions | Min | Тур | Max | Units |
| Output Current | I _o | Over V _{in} range | 0 | 2.0 | 3.0 | Amps |
| Current Limit | l _{c!} | V _{in} =V _o +3V | - | 4.0 | - | Amps |
| Short Circuit Current | l _{sc} | $V_n = V_o + 3V$ | - | TBD | - | Amps |
| Input Voltage Range | V _{in} | l _o = 0.1 to 2.0 Amp | 6.6 | - | 20 | VDC |
| Reflected Ripple | l _{rr} | V _{in} =V _o +5VDC, I _o =2 Amp Z _{source} =1.0Ω | - | TBD | _ | mA _{pp} @ 1MHz |
| Static Voltage Tolerence | V _o | Over V _n range, I _o =2 Amp T _a =-40 to +55°C | - | ±1.0 | ±2.0 | %V。 |
| Ripple Rejection | RR | Over V _n range @ 120 Hz | • | 45 | - | dB |
| Line Regulation | Regine | Over V _n range | - | ±1.0 | ±2.0 | %V。 |
| Load Regulation | Reg _{load} | 0.1 ≤ I _o ≤ 2.0 Amp | - | ±0.2 | ±0.4 | %V。 |
| Ripple/Noise | V _n | $V_{in} = +8V$, $I_{o} = 2$ Amp, $V_{o} = +5V$ | | 30 | - | mV _{pp} |
| Transient Response (with 120µF output cap) | t _{tr} | 50% load change V _o ≤ 1% recovery | • | 100 | • | μSec |
| Efficiency | n | V _{in} =+8V, I _o =2 Amp | | 85 | - | % |
| Switching Frequency | f _o | Over V _n and I _o ranges | 0.95 | 1.0 | 1.05 | MHz |

| Characteristics | w | | 7 | 78\$T305PC | | |
|-------------------------------------|---------|------------------------------------------------|-----|------------|-----|-------|
| (T _a =25°C unless noted) | Symbols | Conditions | Min | Тур | Max | Units |
| Operating Temperature | Ta | Over V _{in} and I _o ranges | -40 | - | +55 | °C |
| Storage Temperature | Ts | | -65 | - | 150 | °C |
| Mechanical Shock | | | - | - | TBD | G's |
| Weight | | | - | 7.0 | - | grams |
| Relative Humidity | | | 0 | - | 95 | % |



PIN-OUT INFORMATION (As viewed from the finned side)

Pin 1 INPUT (right)

2 COMMON (center)

3 OUTPUT (left)

| ORDERING INFORMATION 78ST3xxyC | | | | |
|-----------------------------------|---------------------------|--|--|--|
| xx = Output Volts | y = Package Style | | | |
| 05 = 5.0 Volts | P = Plastic Horizontal | | | |

Important Note:

For proper operation, the 78ST305PC must be used with a $120\mu F$ electrolytic capacitor wired from the output to ground.



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